Network Algebra for Asynchronous Dataflow*

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Abstract

Network algebra is proposed as a uniform algebraic framework for the description and analysis of dataflow networks. An equational theory of networks, called BNA (Basic Network Algebra), is presented. BNA, which is essentially a part of the algebra of flownomials, captures the basic algebraic properties of networks. For asynchronous dataflow networks, additional constants and axioms are given; and a corresponding process algebra model is introduced. This process algebra model is compared with previous models for asynchronous dataflow.

Keywords & Phrases: dataflow networks, network algebra, process algebra, asynchronous dataflow, feedback, merge anomaly, history models, oracle based models, trace models.

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1 Introduction

In this paper we pursue an axiomatic approach to the theory of dataflow networks. Network algebra is presented as a general algebraic setting for the description and analysis of dataflow networks. A network can be any labelled directed hypergraph that represents some kind of flow between the components of a system. For example, flowcharts are networks concerning flow of control and dataflow networks are networks concerning flow of control and dataflow networks are networks concerning flow of data. Assuming that the components have a fixed number of input and output ports, such networks can be built from their components and (possibly branching) connections using parallel composition (+), sequential composition (\circ) and feedback (\uparrow). The connections needed are at least the identity (I) and transposition (X) connections, but additional constants for branching connections are needed for specific classes of networks.

First of all, an equational theory concerning networks that can be built using the above-mentioned operations with only the identity and transposition constants is presented. This theory, called BNA (Basic Network Algebra), is the core of network algebra. For specific classes of networks, additional constants and axioms are needed. Flowcharts constitute one such class. BNA is essentially a part of the algebra of flownomials of Căzănescu and Ștefănescu [2] which was developed for the description and analysis of flowcharts.

A process algebra model of BNA is given as well. Thus, a very straightforward connection between network algebra and process algebra is provided. Process algebra is closely related to programming, whereas network algebra is used for describing systems as a network of interconnected components. A clear connection between them appears to be useful.

After that, an extension of BNA for asynchronous dataflow networks is presented. The specialization of the process algebra model of BNA for this class of networks is also given. This specialized model is additionally connected with various previous models of asynchronous dataflow networks, including Kahn's history model [3], Broy's oracle based models [4], and Jonsson's trace model [5].

For the process algebra models, ACP (Algebra of Communicating Processes) of Bergstra and Klop [6] is used, with the silent step and abstraction, as well as the following additional features: renaming, conditionals, iteration, prefixing and communication free merge.

There are strong connections between the work presented in this paper and other recent work. SCAs (Synchronous Concurrent Algorithms), introduced by Thompson and Tucker in [7], can be described in an extension of BNA for synchronous dataflow networks. In [8], Barendregt et al. present a model of computable processes which is essentially a model of BNA; but a slightly different choice of primitive operations and constants is used. It is also worth mentioning that the examples of Brock and Ackermann [9] and Russell [10] demonstrating a time anomaly in asynchronous dataflow networks are presented in a concise way in this paper, using network algebra for describing the networks of cells and wires and using process algebra for describing the atomic cells.

The paper starts with an outline of network algebra (Section 2) and some process algebra preliminaries (Section 3). Next the signature, the axioms and two models of BNA, including a general process algebra model, are presented (Section 4). Thereafter the signature, the axioms and the process algebra model of the network algebra for asynchronous dataflow are presented (Section 5). Several more abstract models are derived from the process algebra model and compared (Section 6). The non-standard mathematical notation for sets, sequences and tuples used in this paper is explained in an appendix.

2 Overview of network algebra

This section gives an idea of what network algebra is. The meaning of its operations and constants is explained informally making use of a graphical representation of networks. Besides, dataflow networks are presented as a specific class of networks and the further subdivision into synchronous and asynchronous dataflow networks is explained in broad outline. The formal details will be treated in subsequent sections.

2.1 General

In the first place, the meaning of the operations and constants of BNA mentioned in Section 1 (++, \circ , \uparrow , I and X) is explained. Following, the meaning of additional constants for branching connections is explained.

It is convenient to use, in addition to the operations and constants of BNA, the extensions \uparrow^m , I_m and ${}^mX^n$ of the feedback operation and the identity and transposition constants. These extensions are defined by the axioms R5–R6, B6 and B8–B9, respectively, of BNA (see Section 4.1, Table 1). They are called the block extensions of the feedback operation and these constants. Block extensions of additional constants for branching connections can be defined in the same vein.

In Figure 1, the meaning of the operations and constants of BNA (including the block extensions) is illustrated by means of a graphical representation of networks. We write $f: k \to l$ to indicate that network f has k input ports and l output ports;



Figure 1: Operations and constants of BNA

 $k \to l$ is called the sort of f. The input ports are numbered $1, \ldots, k$ and the output ports $1, \ldots, l$. In the graphical representation, they are considered to be numbered from left to right. The networks are drawn with the flow moving from top to bottom. In Figure 2, the meaning of (block extensions of) additional constants for branching connections is illustrated by means of a graphical representation. The operations and



Figure 2: Additional constants for branching connections

constants illustrated here allow to represent all networks (cf. [11]).

2.2 Dataflow networks

In the case of dataflow networks, the components are also called cells. The identity connections are called wires and the transposition connections are viewed as crossing wires. The cells are interpreted as processes that consume data at their input ports, compute new data, deliver the new data at their output ports, and then start over again. The sequences of data consumed or produced by the cells of a dataflow network are called streams. The wires are interpreted as queues of some kind. The classical kinds considered are firstly queues that never contain more than one datum and let data pass through them with a negligible delay, and secondly queues that are able to contain an arbitrary number of data and let data pass through them with a time delay. We call them minimal stream delayers and stream delayers, respectively.

In synchronous dataflow networks, the wires are minimal stream delayers. Basic to synchronous dataflow is that computation is driven by ticks of a global clock. The underlying idea of synchronous dataflow is that computation takes a good deal of time, whereas storage and transport of data takes a negligible deal of time.

In asynchronous dataflow networks, the wires are stream delayers. Basic to asynchronous dataflow is that computation is driven by the arrival of the data needed. The underlying idea of asynchronous dataflow is that computation as well as storage and transport of data take a good deal of time. In asynchronous dataflow networks, cells may independently consume data from their input ports, compute new data, and deliver the new data at their output ports. This means that there may be data produced by some cells but not yet consumed by other cells. Therefore the wires have to be able to buffer an arbitrary amount of data.

Dataflow networks also need branching connections. Because there is a flow of data which is everywhere in the network, the interpretation of the branching connections is not immediately clear. At least two kinds of interpretation can be considered. For the binary branching connections, they are the *copy/equality test* interpretation and the *split/merge* interpretation. The first kind of interpretation fits in with the idea of permanent flows of data which naturally go in all directions at branchings. Synchronous dataflow reflects this idea most closely. The second kind of interpretation fits in with the idea of intermittent flows of data which go in one direction at branchings. Asynchronous dataflow reflects this idea better. In order to distinguish between the branching constants with these different interpretations, different symbols are used.

Dataflow networks have been extensively studied, see e.g. [3, 4, 5, 8, 9, 10, 12, 13].

3 Process algebra preliminaries

This section gives a brief summary of the ingredients of process algebra which make up the basis for the process algebra models presented in Sections 4 and 5. We will suppose that the reader is familiar with them. Appropriate references to the literature are included.

We will make use of ACP, introduced in [6], extended with the silent step τ and the abstraction operator τ_I for abstraction. Semantically, we adopt the approach to abstraction, originally proposed for ACP in [14], which is based on branching bisimulation. ACP with this kind of abstraction is called ACP^{τ}. In ACP with abstraction, processes can be composed by sequential composition, written $P \cdot Q$, alternative composition, written P + Q, parallel composition, written $P \parallel Q$, encapsulation, written $\partial_H(P)$, and abstraction, written $\tau_I(P)$. We will also use the following abbreviation. Let $(P_i)_{i \in \mathcal{I}}$ be an indexed set of process expressions where $\mathcal{I} = \{i_1, \ldots, i_n\}$. Then, we write $\sum_{i \in \mathcal{I}} P_i$ for $P_{i_1} + \ldots + P_{i_n}$. We further use the convention that $\sum_{i \in \mathcal{I}} P_i$ stands for δ if $\mathcal{I} = \emptyset$. For a systematic introduction to ACP, the reader is referred to [15].

Further we will use the following extensions:

- **renaming** We need the possibility of renaming actions. We will use the renaming operator ρ_f , added to ACP in [16]. Here f is a function that renames actions into actions, δ or τ . The expression $\rho_f(P)$ denotes the process P with every occurrence of an action a replaced by f(a). So the most crucial equation from the defining equations of the renaming operator is $\rho_f(a) = f(a)$.
- **conditionals** We will use the two-armed conditional operator $\triangleleft \bullet \triangleright$ as in [17]. The expression $P \triangleleft b \triangleright Q$, is to be read as if b then P else Q. The defining equations are $P \triangleleft t \triangleright Q = P$ and $P \triangleleft f \triangleright Q = Q$. Besides, we will use the one-armed conditional operator : \rightarrow as in [17]. It is defined by $b :\rightarrow P = P \triangleleft b \triangleright \delta$.
- iteration We will also use the binary version of Kleene's star operator *, added to ACP in [18], with the defining equation $P^*Q = P \cdot (P^*Q) + Q$. The behaviour of P^*Q is zero or more repetitions of P followed by Q.
- early input and process prefixing We will additionally use early input action prefixing and the extension of this binding construct to process prefixing, both added to ACP in [19]. Early input action prefixing is defined by the equation $er_i(x)$; $P = \sum_{d \in D} r_i(d) \cdot P[d/x]$. We use the extension to processes mainly to express parallel input: $(er_1(x_1) \parallel \ldots \parallel er_n(x_n))$; P. We have:

$$(er_1(x_1) \parallel er_2(x_2)); P = \sum_{\substack{d_1 \in D \\ d_2 \in D}} r_1(d_1) \cdot (er_2(x_2); P[d_1/x_1]) \\ + \sum_{\substack{d_2 \in D \\ d_2 \in D}} r_2(d_2) \cdot (er_1(x_1); P[d_2/x_2])$$

$$(er_1(x_1) \parallel er_2(x_2) \parallel er_3(x_3)); P = \sum_{\substack{d_1 \in D \\ d_1 \in D}} r_1(d_1) \cdot ((er_2(x_2) \parallel er_3(x_3)); P[d_1/x_1]) \\ + \sum_{\substack{d_2 \in D \\ d_2 \in D}} r_2(d_2) \cdot ((er_1(x_1) \parallel er_3(x_3)); P[d_2/x_2]) \\ + \sum_{\substack{d_3 \in D \\ d_3 \in D}} r_3(d_3) \cdot ((er_1(x_1) \parallel er_2(x_2)); P[d_3/x_3])$$

etc.

communication free merge We will not only use the merge operator (||) of ACP, but also the communication free merge operator (|||). The communication free merge operator can be viewed as a special instance of the synchronisation merge operator $||_H$ of CSP, also added to ACP in [19], viz. the instance for $H = \emptyset$. It is defined by P ||| Q = P ||| Q + Q ||| P, where ||| is defined as || except that $a \cdot P ||| Q = a \cdot (P ||| Q)$. Communication free merge can also be expressed in terms of parallel composition, encapsulation and renaming.

4 Basic network algebra

BNA is common to various classes of networks. In particular, it is common to flowcharts and dataflow networks. The additional constants, needed for branching connections, differ however from one class to another. In this section, BNA is presented. First of all, the signature and axioms of BNA are given. The extension of BNA to the algebra of flownomials is also briefly addressed. In addition, two models of BNA are described: a data transformer model and a process algebra model. In a subsequent section, the extension of BNA for asynchronous dataflow networks is provided.

4.1 Signature and axioms of BNA

Signature

In network algebra, networks are built from other networks – starting with atomic components and a variety of connections. Every network f has a sort $k \to l$, where $k, l \in \mathbb{N}$, associated with it. To indicate this, we use the notation $f: k \to l$. The intended meaning of the sort $k \to l$ is the set of networks with k input ports and l output ports. So $f: k \to l$ expresses that f has k input ports and l output ports.

The sorts of the networks to which an operation of network algebra is applied determine the sort of the resulting network. In addition, there are restrictions on the sorts of the networks to which an operation can be applied. For example, sequential composition can not be applied to two networks of arbitrary sorts because the number of output ports of one should agree with the number of input ports of the other.

The signature of BNA is as follows:

Name	Symbol	Arity
Operations:		
parallel composition	++-	$(k \to l) \times (m \to n) \to (k + m \to l + n)$
sequential composition	0	$(k \to l) \times (l \to m) \to (k \to m)$
feedback	\uparrow	$(m+1 \to n+1) \to (m \to n)$
Constants:		
identity	T	$1 \rightarrow 1$
transposition	Х	$2 \rightarrow 2$

Here k, l, m, n range over N. This means, for example, that there is an instance of the sequential composition operator for each $k, l, m \in \mathbb{N}$.

As mentioned in Section 2, we will also use the block extensions of feedback, identity and transposition. The arity of these auxiliary operations and constants is as follows:

Symbol	Arity
\uparrow^l	$(m+l \rightarrow n+l) \rightarrow (m \rightarrow n)$
I_m	$m \rightarrow m$
$^{m}X^{n}$	$m + n \rightarrow n + m$

Axioms

The axioms of BNA are given in Table 1. The axioms B1–B6 for $+, \circ$ and I_m define

f + (g + h) = (f + g) + hR1 $g \circ (f \uparrow^m) = ((g + \mathbf{I}_m) \circ f) \uparrow^m$ B1 $I_0 + f = f = f + I_0$ R2 $(f\uparrow^m)\circ g = (f\circ(g + \mathbf{I}_m))\uparrow^m$ B2 $f\circ (g\circ h)=(f\circ g)\circ h$ R3 $f + (g \uparrow^m) = (f + g) \uparrow^m$ B3R4 $(f \circ (\mathsf{I}_l + g)) \uparrow^m = ((\mathsf{I}_k + g) \circ f) \uparrow^n$ B4 $\mathsf{I}_k \circ f = f = f \circ \mathsf{I}_l$ $(f + f') \circ (g + g') = (f \circ g) + (f' \circ g')$ for $f: k+m \to l+n, g: n \to m$ B5 $\mathsf{I}_k + \mathsf{I}_l = \mathsf{I}_{k+l}$ $f\uparrow^0=f$ B6R5 ${}^{k}\mathsf{X}^{l}\circ{}^{l}\mathsf{X}^{k}=\mathsf{I}_{k+l}$ R6 $(f\uparrow^l)\uparrow^k = f\uparrow^{k+l}$ B7 ${}^{k}\mathsf{X}^{0} = \mathsf{I}_{k}$ B8 ${}^{k}\mathsf{X}^{l+m} \stackrel{\sim}{=} ({}^{k}\mathsf{X}^{l} + \mathsf{I}_{m}) \circ (\mathsf{I}_{l} + {}^{k}\mathsf{X}^{m})$ B9B10 $(f + g) \circ {}^m \mathsf{X}^n = {}^k \mathsf{X}^l \circ (g + f)$ F1 $I_k \uparrow^k = I_0$ F2 ${}^{k}\mathsf{X}^{k}\uparrow^{k}=\mathsf{I}_{k}$ for $f: k \to m, g: l \to n$

Table 1: Axioms of BNA

a strict monoidal category; and together with the additional axioms B7–B10 for ${}^{m}X^{n}$, they define a *symmetric* strict monoidal category. The remaining axioms R1–R6 and F1–F2 characterize \uparrow^{l} . The axioms R5–R6, B6 and B8–B9 can be regarded as the defining equations of the block extensions of \uparrow , I and X, respectively.

The axioms of BNA are sound and complete for networks modulo graph isomorphism (cf. [11]). Using the graphical representation of Section 2.1, it is easy to see that

the axioms in Table 1 are sound. By means of the axioms of BNA, each expression can be brought into a normal form

$$((\mathsf{I}_m + x_1 + \ldots + x_k) \circ f) \uparrow^{m_1 + \ldots + m_k}$$

where the $x_i : m_i \to n_i$ $(i \in [k])$ are the atomic components of the network and $f: m+n_1+\ldots+n_k \to n+m_1+\ldots+m_k$ is a bijective connection. A network is uniquely represented by a normal form expression up to a permutation of x_1, \ldots, x_k . The completeness of the axioms of BNA now follows from the fact that these permutations in a normal form expression are deducible from the axioms of BNA as well.

As a first step towards the models for asynchronous dataflow networks described in Sections 5 and 6, a data transformer model and a process algebra model of BNA are provided immediately after the connection with the algebra of flownomials has been addressed.

Extension to the algebra of flownomials

The algebra of flownomials is essentially¹ a conservative extension of BNA. Recall that the algebra of flownomials was not developed for dataflow networks, but for flowcharts. The signature of the algebra of flownomials is obtained by extending the signature of BNA as follows with additional constants for branching connections:

Name	Symbol	Arity	Instances
Additional constants:			
ramification	\wedge_k	$1 \rightarrow k$	$\left\{ \begin{array}{ll} \wedge & := & \wedge_2 \\ \bot & := & \wedge_0 \end{array} \right.$
identification	\vee^k	$k \rightarrow 1$	$\left\{ \begin{array}{ll} \vee & := & \vee^2 \\ \top & := & \vee^0 \end{array} \right.$

We will restrict our attention to the instances for k = 0 and k = 2, i.e. \land, \bot, \lor and \top . The other instances can be defined in terms of them:

$$\wedge_{k+1} = \wedge \circ (\wedge_k + \mathbf{I})$$
$$\vee^{k+1} = (\vee^k + \mathbf{I}) \circ \vee$$

It follows from these definitions, together with the axioms A3 and A7 of the algebra of flownomials (see Table 2), that $\wedge_1 = \vee^1 = \mathsf{I}$.

We will use the block extensions of \land , \bot , \lor and \top . The arity of these auxiliary constants is as follows:

Symbol	Arity
\wedge^m	$m \rightarrow 2m$
\bot^m	$m \rightarrow 0$
\vee_m	2m ightarrow m
$ op_m$	$0 \rightarrow m$

¹For naming ports, an arbitrary monoid is used in the algebra of flownomials whereas the monoid of natural numbers is used in BNA.

The axioms for the additional constants of the algebra of flownomials are given in Table 2. These axioms where chosen in order to describe the branching structure of

 $\begin{array}{ll} \mathrm{A5} & \wedge^m \circ (\wedge^m \# \mathsf{I}_m) = \wedge^m \circ (\mathsf{I}_m \# \wedge^m) \\ \mathrm{A6} & \wedge^m \circ {}^m \mathsf{X}^m = \wedge^m \end{array}$ $(\vee_m \, \, \# \, \mathsf{I}_m) \circ \vee_m = (\mathsf{I}_m \, \, \# \, \vee_m) \circ \vee_m$ A1 ${}^{\bar{m}}\mathsf{X}^{m}\circ\vee_{m}=\vee_{m}$ A2A7 $\wedge^m \circ (\perp^m + \mathsf{I}_m) = \mathsf{I}_m$ A3 $(\top_m + \mathbf{I}_m) \circ \vee_m = \mathbf{I}_m$ $\bigvee_m \circ \bot^m = \bot^m + \bot^m$ A8 $\top_m \circ \wedge^m = \top_m \# \top_m$ A4A9 $\top_m \circ \bot^m = \mathsf{I}_0$ A10 $\vee_m \circ \wedge^m = (\wedge^m + \wedge^m) \circ (\mathbf{I}_m + {}^m \mathbf{X}^m + \mathbf{I}_m) \circ (\vee_m + \vee_m)$ A11 $\wedge^m \circ \vee_m = \mathsf{I}_m$ A16 $\perp^0 = \mathsf{I}_0$ A12 $\top_0 = \mathsf{I}_0$ A17 $\perp^{m+n} = \perp^m + \perp^n$ A13 $\top_{m+n} = \top_m \# \top_n$ A18 $\wedge^0 = \mathsf{I}_0$ A14 $\vee_0 = \mathsf{I}_0$ A15 $\vee_{m+n} = (\mathsf{I}_m + {}^n\mathsf{X}^m + \mathsf{I}_n) \circ (\vee_m + \vee_n)$ A19 $\wedge^{m+n} = (\wedge^m + \wedge^n) \circ (\mathsf{I}_m + {}^m\mathsf{X}^n + \mathsf{I}_n)$ F4 $\wedge^m \uparrow^m = \top_m$ $\vee_m \uparrow^m = \bot^m$ F3 F5 $((\mathsf{I}_m + \wedge^m) \circ (^m \mathsf{X}^m + \mathsf{I}_m) \circ (\mathsf{I}_m + \vee_m)) \uparrow^m = \mathsf{I}_m$

Table 2: Additional axioms for flowcharts

flowcharts. The axioms A12–A19 can be regarded as the defining equations of the block extentions of \land , \bot , \lor and \top .

The standard model for the interpretation of flowcharts is the model $\operatorname{\mathsf{Rel}}(D)$ of relations over a set D (cf. [2, 20]). All axioms of the algebra of flownomials (Tables 1 and 2) hold in this model.

4.2 Data transformer model of BNA

In this subsection, a data transformer model is described. A parallel data transformer $f: m \to n$ acts on an *m*-tuple of input data and produces an *n*-tuple of output data. Parallel composition, sequential composition and feedback operators as well as identity and transposition constants are defined on parallel data transformers. All axioms of BNA (Table 1) hold in the resulting model.

Definition 4.1 (data transformer model of BNA) A parallel data transforming relation $f \in \text{Rel}(S)(m, n)$ is a relation

$$f \subseteq S^m \times S^n$$

where S is a set of data. $\operatorname{Rel}(S)$ denotes the indexed family of data transforming relations $(\operatorname{Rel}(S)(m,n))_{\mathbb{N}\times\mathbb{N}}$.

The operations and constants of BNA are defined on Rel(S) as follows:

Name	Notation			
parallel composition	f + g	$q \in Rel(S)(m+p,n+q)$	for $f \in Rel(S)(m,n), g \in Rel(S)(p,q)$	
sequential composition	$f\circ g$	$\in Rel(S)(m,p)$	for $f \in Rel(S)(m,n), g \in Rel(S)(n,p)$	
feedback	$f\uparrow^p$	$\in Rel(S)(m,n)$	for $f \in Rel(S)(m+p, n+p)$	
identity	I_n	$\in Rel(S)(n,n)$		
transposition	${}^{m}X^{n}$	$\in Rel(S)(m+n,n+m)$		

Definition

$$\begin{array}{lll} f \mbox{ + }g &=& \{\langle x^{\frown}y, z^{\frown}w \rangle \mid x \in S^m, y \in S^p, z \in S^n, w \in S^q, \langle x, z \rangle \in f \land \langle y, w \rangle \in g\} \\ f \circ g &=& \{\langle x, y \rangle \mid x \in S^m, y \in S^p, \exists z \in S^n \cdot \langle x, z \rangle \in f \land \langle z, y \rangle \in g\} \\ f \uparrow^p &=& \{\langle x, y \rangle \mid x \in S^m, y \in S^n, \exists z \in S^p \cdot \langle x^{\frown}z, y^{\frown}z \rangle \in f\} \\ \mathbf{I}_n &=& \{\langle x, x \rangle \mid x \in S^n\} \\ ^m \mathbf{X}^n &=& \{\langle x^{\frown}y, y^{\frown}x \rangle \mid x \in S^m, y \in S^n\} \end{array}$$

These definitions are very straightforward. Note that this data transformer model has a global crash property: if a component of a network fails to produce output, the whole network fails to produce output.

Theorem 4.2 ($\operatorname{Rel}(S), +, \circ, \uparrow, I, X$) is a model of BNA.

Proof: The proof is a matter of straightforward calculation using only elementary set theory. \Box

4.3 Process algebra model of BNA

Network algebra can be regarded as being built on top of process algebra.

Let D be a fixed, but arbitrary, set of data. D is a parameter of the model. The processes use the standard actions $r_i(d)$, $s_i(d)$ and $c_i(d)$ for $d \in D$ only. They stand for read, send and communicate, respectively, datum d at port i. On these actions, communication is defined such that $r_i(d) | s_i(d) = c_i(d)$ (for all $i \in \mathbb{N}$ and $d \in D$). In all other cases, it yields δ .

We write H(i), where $i \in \mathbb{N}$, for the set $\{r_i(d) \mid d \in D\} \cup \{s_i(d) \mid d \in D\}$ and I(i) for $\{c_i(d) \mid d \in D\}$. In addition, we write H(i, j) for $H(i) \cup H(j)$, H(i + [k]) for $H(i + 1) \cup \ldots \cup H(i + k)$ and H(i + [k], j + [l]) for $H(i + [k]) \cup H(j + [l])$. The abbreviations I(i, j), I(i + [k]) and I(i + [k], j + [l]) are used analogously.

in(i/j) denotes the renaming function defined by

$$in(i/j)(r_i(d)) = r_j(d) \text{ for } d \in D$$

$$in(i/j)(a) = a \text{ for } a \notin \{r_i(d) \mid d \in D\}$$

So in(i/j) renames port *i* into *j* in read actions. out(i/j) is defined analogously, but renames send actions. We write in(i+[k]/j+[k]) for $in(i+1/j+1)\circ\ldots\circ in(i+k/j+k)$ and in([k]/j+[k]) for in(0+[k]/j+[k]). The abbreviations out(i+[k]/j+[k]) and out([k]/j+[k]) are used analogously.

Definition 4.3 (process algebra model of BNA) A *network* $f \in \text{Proc}(D)(m, n)$ is a triple

f = (m, n, P)

where P is a process with actions in $\{r_i(d) \mid i \in [m], d \in D\} \cup \{s_i(d) \mid i \in [n], d \in D\}$. $\mathsf{Proc}(D)$ denotes the indexed family of sets $(\mathsf{Proc}(D)(m,n))_{\mathbb{N} \times \mathbb{N}}$.

A wire is a network $I = (1, 1, w_1^1)$, where w_1^1 satisfies:

for all networks f = (m, n, P) and $u, v > \max(m, n)$,

(P1)	$\tau_{I(u,v)}(\partial_{H(v,u)}(w_v^u \parallel w_u^v)) \parallel P = P$			
(P2)	$\tau_{I(u,v)}(\partial_{H(u,v)}((\rho_{in(i/u)}(P)\parallel\!\!\parallel w_v^i)\parallel w_u^v))=P$	for all $i \in [m]$		
(P3)	$\tau_{I(u,v)}(\partial_{H(u,v)}((\rho_{out(j/v)}(P)\parallel\!\mid w_j^u)\parallel w_u^v))=P$	for all $j \in [n]$		
where $w_v^u = \rho_{in(1/u)}(\rho_{out(1/v)}(w_1^1))$				

The operations and constants of BNA are defined on $\mathsf{Proc}(D)$ as follows:

Name	Notati	on	
parallel composition	f + g	$\in Proc(D)(m+p,n+q)$	for $f \in Proc(D)(m, n), g \in Proc(D)(p, q)$
sequential composition	$f\circ g$	$\in Proc(D)(m,p)$	for $f \in Proc(D)(m,n), g \in Proc(D)(n,p)$
feedback	$f\uparrow^p$	$\in Proc(D)(m,n)$	for $f \in Proc(D)(m+p, n+p)$
identity	I_n	$\in Proc(D)(n,n)$	
transposition	$^{m}X^{n}$	$\in Proc(D)(m+n,n+m$)

Definition

$$\begin{split} &(m,n,P) + (p,q,Q) \ = \ (m+p,n+q,R) \quad \text{where } R = P \parallel \rho_{in([p]/m+[p])}(\rho_{out([q]/n+[q])}(Q)) \\ &(m,n,P) \circ (n,p,Q) \ = \ (m,p,R) \quad \text{where, for } u = \max(m,p), v = u + n, \\ &R = \tau_{I(u+[n],v+[n])}(\partial_{H(u+[n],v+[n])}((\rho_{out([n]/u+[n])}(P) \parallel \rho_{in([n]/v+[n])}(Q)) \parallel w_{v+1}^{u+1} \parallel \dots \parallel w_{v+n}^{u+n})) \\ &(m+p,n+p,P) \uparrow^p \ = \ (m,n,Q) \quad \text{where, for } u = \max(m,n), v = u + p, \\ &Q = \tau_{I(u+[p],v+[p])}(\partial_{H(u+[p],v+[p])}(\rho_{in(m+[p]/v+[p])}(\rho_{out(n+[p]/u+[p])}(P)) \parallel w_{v+1}^{u+1} \parallel \dots \parallel w_{v+p}^{u+p})) \\ &\mathbf{I}_n \ = \ (n,n,P) \quad \text{where } P = w_1^1 \parallel \dots \parallel w_n^n & \text{if } n > 0 \\ &\tau_{I(1,2)}(\partial_{H(1,2)}(w_2^1 \parallel w_1^2)) & \text{otherwise} \end{split}$$

The conditions (P1)–(P3) are rather obscure at first sight, but see the remark at the end of this section. The definitions of sequential composition and feedback illustrate clearly the differences between the mechanisms for using ports in network algebra and process algebra. In network algebra the ports that become internal after composition are hidden. In process algebra based models these ports are still visible; a special operator must be used to hide them. For typical wires, $\tau_{I(1,2)}(\partial_{H(1,2)}(w_2^1 \parallel w_1^2))$ equals δ or $\tau \cdot \delta$.

In the description of a process algebra model of BNA given above, all constants and operators used are common to ACP^{τ} and its discrete time extension with relative timing, called ACP_{drt}^{τ} [21], or belong to a few of their mutual (conservative) extensions mentioned in Section 3 (viz. renaming and communication free merge). As a result, we can specialize this general model for a specific kind of networks using either ACP^{τ} or ACP_{drt}^{τ} ; with further extensions at need.

Theorem 4.4 $(\operatorname{Proc}(D), +, \circ, \uparrow, \mathsf{I}, \mathsf{X})$ is a model of BNA.

Proof: According to [22], there is an algebra equivalent to BNA (the algebra of LR-flow over **B**i), but having two renumbering operations, for (bijectively) renumbering input ports and output ports, instead of the transposition constant and the sequential composition operation of BNA. Renumbering is just renaming in the corresponding process algebra model. The crucial axioms concerning the constant I_n in the equational theory of that algebra follow immediately from the conditions (P1)–(P3) on wires in Definition 4.3. For quite a few axioms from this equational theory, the proof that they are satisfied by the process algebra model is a matter of simple calculation using only elementary properties of renaming, communication free merge, or parallel composition and renaming. For the remaining axioms, reminiscent of the axioms R1–R4 of BNA, the proof is a matter of straightforward calculation using in addition properties of parallel composition and encapsulation or abstraction. All properties concerned are common to ACP^{τ} and ACP^{τ} or they are properties of the mutual extensions used in Definition 4.3. \Box

So if we select a specific wire, such as \mathbf{sd}_1^1 in Section 5, we have obtained a model of BNA if the conditions (P1)–(P3) are satisfied by the wire concerned. It is worth mentioning that the conditions (P1)–(P3) are equivalent to the axioms B2 and B4 of BNA: (P1) corresponds to $I_0 \# f = f = f \# I_0$, (P2) to $I_m \circ f = f$, and (P3) to $f = f \circ I_n$.

5 Asynchronous dataflow networks

In this section, an extension of BNA for asynchronous dataflow networks is presented. In the first place, the additional constants and axioms for asynchronous dataflow are given. After that, the specialization of the process algebra model of Section 4.3 for asynchronous dataflow networks is described. The adaptation of the data transformer model of Section 4.2 to asynchronous dataflow networks is not described here. Instead, the problem with this model and its proposed solutions are outlined. In Section 6.1, the model concerned is derived from the process algebra model described in this

section. Some models that have been proposed as alternatives are derived there as well.

Various models for asynchronous dataflow have been proposed and the valid axioms differ from one model to another. The axioms given here are valid in the presented process algebra model for asynchronous dataflow in case the split/merge interpretation is used for the branching connections. We stress here on the point that we do not present axioms valid in all proposed models for asynchronous dataflow. Neither do we claim completeness with respect to the presented model.

5.1 Additional constants and axioms

The signature of the extension of BNA for asynchronous dataflow networks is obtained by extending the signature of BNA as follows with additional constants for branching connections:

Name	Symbol	Arity
Additional constants:		
split	\wedge^m	$m \rightarrow 2m$
sink	$ightarrow^m$	$m \rightarrow 0$
merge	\bigvee_m	$2m \rightarrow m$
dummy source	ightharpoonup m	$0 \rightarrow m$
asynchronous copy	\mathbb{R}^m	$m \rightarrow 2m$
asynchronous equality test	\forall_m	2m ightarrow m

The symbols \bigwedge^m , \bigvee_m , indicating the split/merge interpretation, as well as the symbols \bigotimes^m and \bigvee_m , indicating the copy/equality test interpretation, are used here. Although the former interpretation seems more close to asynchronous dataflow than the latter interpretation, both are found in asynchronous dataflow.

In Table 3, axioms for the additional constants \wedge^m , \flat^m , \flat_m and \P_m are given. We consider the axioms in Table 3 desired axioms for asynchronous dataflow networks. They are all valid in the process algebra model described below, but not in some other models. For example, axiom A3 is not valid in Broy's oracle based models [4]. The axioms for the constants \wedge, \flat, \forall and \P are the same as the ones in case of synchronous dataflow networks (see [1], Table 3).

In the next subsection, the process algebra model introduced in Section 4 is specialized to describe the semantics of asynchronous dataflow networks.

5.2 Process algebra model for asynchronous dataflow

In this subsection, the specialization of the process algebra model of BNA (Section 4.3) for asynchronous dataflow networks is given.

In Section 4.3, only a few assumption about wires and atomic cells were made. Here these ingredients are actualized for asynchronous dataflow networks.

Definition 5.1 (wires and atomic cells in asynchronous dataflow networks)

 $(\checkmark_m + \mathsf{I}_m) \circ \checkmark_m = (\mathsf{I}_m + \curlyvee_m) \circ \checkmark_m$ A5A1 ${\bigstar}^m \circ {}^m {\mathsf{X}}^m = {\bigstar}^m$ ${}^m\mathsf{X}^m\circ {}\bigstar_m={}\bigstar_m$ A2A6 $\stackrel{(*)}{\P}_m \circ \bigwedge^m = \P_m \, \# \, \P_m$ $(\mathbf{\uparrow}_m + \mathbf{I}_m) \circ \mathbf{\lor}_m = \mathbf{I}_m$ A3A7 $\bigvee_m \circ \downarrow^m = \downarrow^m + \downarrow^m$ A8 A4A9 $\mathbf{P}_m \circ \mathbf{P}^m = \mathbf{I}_0$ A10 (*) A11 (*) $\begin{array}{ll} \mathrm{A16} & \mathbf{b}^0 = \mathbf{I}_0 \\ \mathrm{A17} & \mathbf{b}^{m+n} = \mathbf{b}^m \ \mathrm{\#} \ \mathbf{b}^n \end{array}$ A12 $\mathbf{\uparrow}_0 = \mathbf{I}_0$ A13 $\mathbf{q}_{m+n} = \mathbf{q}_m + \mathbf{q}_n$ A18 $\wedge^0 = \mathbf{I}_0$ A14 $\bigvee_0 = \mathsf{I}_0$ $\bigvee_{m+n} = (\mathsf{I}_m + {^n}\mathsf{X}^m + \mathsf{I}_n) \circ (\bigvee_m + \bigvee_n) \quad \text{A19} \quad \bigwedge^{m+n} = (\bigwedge^m + \bigwedge^n) \circ (\mathsf{I}_m + {^m}\mathsf{X}^n + \mathsf{I}_n)$ A15 $\bigvee_m \uparrow^m = b^m$ F4 ${\bigstar}^m\uparrow^m=\P_m$ F3 F5 (*)

Table 3: Additional axioms for asynchronous dataflow networks

The identity constant, called the *stream delayer*, is the wire $I_1 = (1, 1, \mathbf{sd}_1^1(\varepsilon))$, where \mathbf{sd}_1^1 is defined by

$$\operatorname{sd}_1^1(\sigma) = er_1(x) ; \operatorname{sd}_1^1(\sigma x) + |\sigma| > 0 :\to s_1(hd(\sigma)) \cdot \operatorname{sd}_1^1(tl(\sigma))$$

The deterministic cell computing a function $f: D^m \to D^n$ is the network $C_f = I_m \circ (m, n, P_f) \circ I_n$ where P_f is defined by

$$P_f = ((er_1(x_1) \parallel \dots \parallel er_m(x_m)); s_1(f_1(x_1, \dots, x_m)) \parallel \dots \parallel s_n(f_n(x_1, \dots, x_m))) * \delta$$

where, for $i \in [n], f_i(x_1, \dots, x_m) = y_i$ if $f(x_1, \dots, x_m) = (y_1, \dots, y_n)$.

The non-deterministic cell computing a (finitely branching) relation $R \subseteq D^m \times D^n$ is the network $C_R = \mathsf{I}_m \circ (m, n, P_R) \circ \mathsf{I}_n$ where P_R is defined by

$$P_{R} = ((er_{1}(x_{1}) \parallel \dots \parallel er_{m}(x_{m})); \\ \tau \triangleleft R(x_{1}, \dots, x_{m}) = \emptyset \triangleright \sum_{(a_{1}, \dots, a_{n}) \in R(x_{1}, \dots, x_{m})} (s_{1}(a_{1}) \parallel \dots \parallel s_{n}(a_{n}))) * \delta$$

The restriction of Proc(D) to the processes that can be built under this actualization is denoted by AProc(D). \Box

The definition of \mathbf{sd}_1^1 simply expresses that it behaves as a queue. The definition of P_f expresses the following. P_f waits until one datum is offered at each of the input ports $1, \ldots, m$. When data is available at all input ports, P_f proceeds with producing data at the output ports $1, \ldots, n$. The datum produced at the *i*-th output port is the *i*-component of the value of the function f for the consumed input tuple. When data is delivered at all output ports, P_f proceeds with repeating itself. The non-deterministic case (P_R) is similar.

For $\mathsf{AProc}(D)$, the operations and constants of BNA as defined on $\mathsf{Proc}(D)$ can be taken with sd_1^1 as wire. This means that only the additional constants for asynchronous dataflow have to be defined. **Definition 5.2** (process algebra model for asynchronous dataflow) The operations +, \circ , \uparrow^n on $\mathsf{AProc}(D)$ are the instances of the ones defined on $\mathsf{Proc}(D)$ for sd_1^1 as wire. Analogously, the constants I_n and ${}^m\mathsf{X}^n$ in $\mathsf{AProc}(D)$ are the instances of the ones defined on $\mathsf{Proc}(D)$ for sd_1^1 as wire.

For n = 1, the additional constants in $\mathsf{AProc}(D)$ are defined as follows:

Name	Notation			
split	$\bigwedge^1 \in AProc(D)(1,2)$			
sink	$b^1 \in AProc(D)(1,0)$			
merge	$\bigvee_1 \in AProc(D)(2,1)$			
dummy source	$\P_1 \in AProc(D)(0,1)$			
asynchronous copy	$\Re^1 \in AProc(D)(1,2)$			
asynchronous equality test	$arget_1 \in AProc(D)(2,1)$			

Definition

\bigstar^1	=	$I_1 \circ (1,2,split^1) \circ I_2$	where $split^{1} = (er_{1}(x); (s_{1}(x) + s_{2}(x))) * \delta$
b^1	=	$I_1 \circ (1,0,sink^1)$	where $sink^1 = (er_1(x); \tau) * \delta$
\bigvee_1	=	$I_2 \circ (2,1,merge_1) \circ I_1$	where $merge_1 = ((er_1(x) + er_2(x)); s_1(x)) * \delta$
1	=	$(0,1, \textit{source}_1) \circ I_1$	where $source_1 = \delta$
\mathbb{R}^1	=	$I_1 \circ (1,2,acopy^1) \circ I_2$	where $acopy^{1} = (er_{1}(x); (s_{1}(x) s_{2}(x))) * \delta$
$arkappa_1$	=	$I_2 \circ (2,1,aeq_1) \circ I_1$	where $aeq_1 = ((er_1(x_1) er_2(x_2)); s_1(x_1) \triangleleft x_1 = x_2 \triangleright s_1(\sqrt{2})) * \delta$

For $n \neq 1$, the constants split, sink, merge and dummy source are defined by the equations occurring as axioms A12–A19 in Table 3 and the constants copy and equality test are defined in the same way as split and merge, respectively. \Box

In order to be fully precise, we have to adapt the definitions given in Section 4.3 in the case of asynchronous dataflow with the equality test as additional constant: all occurrences of the condition $d \in D$ have to be replaced by $d \in D \cup \{\sqrt{\}}$.

Lemma 5.3 The wire $I_1 = (1, 1, \operatorname{sd}_1^1)$ gives an identity flow of data, i.e. for all f = (m, n, P) in $\operatorname{AProc}(D)$, $I_m \circ f = f = f \circ I_n$.

Proof: For I_1 , it is well known that $I_1 \circ I_1 = I_1$ (see e.g. [23]). $I_n \circ I_n = I_n$ and ${}^m X^n \circ I_n = {}^m X^n = I_m \circ {}^m X^n$ follow trivially from $I_1 \circ I_1 = I_1$. So the asserted equations hold for I_n and ${}^m X^n$. Due to the pre- and postfixing with identities in the definitions of the remaining constants and the atomic cells, it follows trivially that these equations hold also for them. The result then follows by induction on the construction of a network in $\mathsf{AProc}(D)$. \Box

Theorem 5.4 (AProc(D), #, \circ , \uparrow , I, X) is a model of BNA. The constants \land , \flat , \lor , \uparrow satisfy the additional axioms for asynchronous dataflow networks (Table 3). The constants \land , \flat , \lor , \uparrow satisfy the additional axioms for synchronous dataflow networks ([1], Table 3).

Proof: A simple calculation shows that $I_0 + f = f = f + I_0$ for all $f \in \mathsf{AProc}(D)$. The first part then follows immediately from Theorem 4.4 and Lemma 5.3. The proof of the second and third part is a matter of tedious, but unproblematic calculation in the style of, for example, [24, 25]. \Box

We do not provide a detailed proof of the second and third part of Theorem 5.4 for various reasons. Different strategies for such a proof are possible, but for each of them the proof will turn out to be a long listing of rather uninteresting calculations. The principal degree of freedom lies in the fraction of formal equational reasoning from axioms versus semantic work directly in the model of process graphs modulo branching bisimulation. A proof within this model will be rather unreadable and still informal. If a proof using equational reasoning is made, one needs a proof system such as the one for μ CRL [26] and a systematic use of the conditional alphabet axioms introduced in [27]. This kind of proofs can be found in [24, 25]. There the proofs have been worked out to the level of detail that they can be formalized in the underlying type theory of the proof assistant **Coq** (see e.g. [28]) and automatically checked. This approach will work as well for the second and third part of Theorem 5.4, i.e. for the network algebra axioms concerned. We have not followed this approach because fully formal proofs would in this case not increase the plausibility of the axioms concerned.

5.3 More abstract models for asynchronous dataflow

A more abstract model based on stream transformers can be given for deterministic asynchronous dataflow networks. Stream transformers can be viewed as data transformers (as defined in Section 4.2) where the data are actually streams of data. A result of Kahn [3] shows that this model is compositional. As shown by Brock and Ackermann [9], and Keller [29], the model is not compositional in the nondeterministic case. Some networks that are equivalent – realize the same relation between their input and output streams – can not be substituted for each other in a larger network because equivalence will get lost.

This deviation, known as the Brock-Ackermann anomaly and the merge anomaly, is a time anomaly. It is related to the feedback operation. Consider an arbitrary deterministic dataflow network with a feedback loop. If the network gets data faster from its feedback loop, the additional data do not change at any moment the prefix of the streams being produced because the network is deterministic. So only the relation between the input and output streams matters. However, in the nondeterministic case, the timing differences in producing the data that is fed back become important. The Brock-Ackermann example relies on such timing differences to show that the feedback of certain networks with the same relation between its input and output streams are different.

One may try to solve the anomaly in two ways:

- (1) weaken the abstract model,
- (2) strengthen the operational model.

On the lines of (1), several models have been proposed [9, 4, 5, 13]. The general approach of these proposals can be described as follows: add more detail to the model, but keep unchanged the operational interpretation of wires as unbounded queues. In this way the simple stream transformer model is sacrificed and other models emerge: trace models giving global time information by merging all the local streams into one trace, oracle based models reducing nondeterministic behaviour to deterministic behaviour up to certain oracles and using the compositionality of the stream transformer model for deterministic dataflow networks, etc. The stream transformer model and some of these more detailed models are the subject of Section 6. The stream transformer model.

6 Related models for asynchronous dataflow

In this section, several different models for asynchronous dataflow are explained from the angle of the process algebra model presented above. First of all, Kahn's history model [3], Broy's oracle based models [4] and Jonsson's trace model [5] are derived from the process algebra model presented in Section 5.2. Next, the time anomaly, which may occur in the history model, is explained using the Brock-Ackermann example. After that, the derived models are broadly compared with each other. Finally, a different process algebra model, based on guess-and-borrow queues, is outlined. With this new operational model, the time anomaly disappears.

6.1 Derivation of related models

In this subsection, the derivation of several models from the process algebra model for asynchronous dataflow is described. Connecting the process algebra model with the history model and the oracle based models, requires a somewhat unnatural reconstruction of these models. We provide a description of it below, but we agree that it does not go smoothly.

A stream is considered to be an element of D^{ω} , i.e. a finite or infinite sequence of data. In order to be able to use well-known models of process algebra in the derivation of the history model, and the oracle based models using the history model, the input streams of a network have to be represented by networks. These input networks are then composed with the original network.

Definition 6.1 (input network)

Let σ be a stream. The input network associated with σ is the network $SOURCE_1(\sigma) = (0, 1, \tau \cdot source_1(\sigma))$ where

$$\operatorname{source}_1(\sigma) = |\sigma| > 0 :\to s_1(hd(\sigma)) \cdot \operatorname{source}_1(tl(\sigma))$$

Let $f: m \to n$ be a network and $\sigma_1, \ldots, \sigma_m$ be streams. The network $f(\sigma_1, \ldots, \sigma_m)$ is defined by

$$f(\sigma_1,\ldots,\sigma_m) = (\texttt{SOURCE}_1(\sigma_1) + \ldots + \texttt{SOURCE}_1(\sigma_m)) \circ f$$

For given input streams, the output streams can be reconstructed from the complete traces of the process corresponding to the composed network as described above. We write trace(P), where P is a process, for the set of complete traces of P. What we are talking about here is the union of the complete traces of P as defined in [30], the traces of P that become complete if we identify livelock nodes (i.e. nodes that only permit an infinite path of silent steps) with deadlock nodes, and the infinite traces of P. trace(P) is formally defined in [31], where it is called the set of fair traces of P. Note however that the distinction between successful termination and deadlock/livelock made in such traces is irrelevant here because the processes modeling asynchronous dataflow networks do not include successfully terminating processes.

Definition 6.2 (stream extraction)

Let β be a trace over $\{s_i(d) \mid i \in [m], d \in D\} \cup \{r_j(d) \mid j \in [n], d \in D\}$. We write streamⁱⁿ_i(β) for the stream of data obtained by first removing all actions that are not of the form $r_i(d)$ and after that replacing each action of the form $r_i(d)$ by d. Analogously, we write stream_i^{out}(β) for the stream of data obtained by first removing all actions that are not of the form $s_i(d)$ and after that replacing each action of the form $s_i(d)$ by d. \Box

For a network $f: m \to n$ and an *m*-tuple of streams $(\sigma_1, \ldots, \sigma_m)$, the possible *n*-tuples of output streams can now be obtained from the traces of the process corresponding to the network $f(\sigma_1, \ldots, \sigma_m)$ using stream extraction.

Definition 6.3 (history relation)

We write trace(f), where f = (m, n, P) is a network, for trace(P). The input-output history relation of a network $f: m \to n$, written [f], is defined by

$$[f](\sigma_1, \ldots, \sigma_m) = \{(\mathsf{stream}_1^{out}(\beta), \ldots, \mathsf{stream}_n^{out}(\beta)) \mid \beta \in \mathsf{trace}(f(\sigma_1, \ldots, \sigma_m))\}$$

The associated equivalence on networks corresponds to Kahn's history model [3]. Hence the following definition.

Definition 6.4 $(\equiv_{\text{history}})$

The history equivalence \equiv_{history} on asynchronous dataflow networks is defined by $f \equiv_{\text{history}} g \text{ iff } [f] = [g]. \ \Box$

Broy's oracle based models [4], which are closely related to Kahn's history model, may be derived from the process algebra model as well. To this end we consider the following merge and split using oracles.

Definition 6.5 (split and merge with oracles) Let $\alpha \in \{1,2\}^{\infty}$ be an oracle. The split and merge constants with oracles are defined on $\mathsf{AProc}(D)$ as follows:

Name	Notation		
split with oracle	$\bigstar^1(\alpha) \in AProc(D)(1,2)$		
merge with oracle	$arphi_1(lpha) \in \operatorname{AProc}(D)(2,1)$		

$$\begin{split} & \wedge^{1}(\alpha) = \mathsf{I}_{1} \circ (1,2,split^{1}(\alpha,0)) \circ \mathsf{I}_{2} \\ & \text{where } split_{1}(\alpha,i) = (er_{1}(x) ; s_{1}(x) \lhd \alpha(i) = 1 \rhd s_{2}(x)) \cdot split_{1}(\alpha,i+1) \\ & \forall_{1}(\alpha) = \mathsf{I}_{2} \circ (2,1,merge_{1}(\alpha,0)) \circ \mathsf{I}_{1} \\ & \text{where } merge_{1}(\alpha,i) = (er_{1}(x) \lhd \alpha(i) = 1 \rhd er_{2}(x) ; s_{1}(x)) \cdot merge_{1}(\alpha,i+1) \end{split}$$

Definition 6.6 (\equiv_{broy} and $\equiv_{\text{broy-fair}}$)

Let $\alpha_1, \ldots, \alpha_k \in \{1, 2\}^{\infty}$ be oracles and let $f(\alpha_1 \ldots \alpha_k)$ be the network obtained form f by replacing each occurrence of \mathbb{A}^1 and \bigvee_1 by $\mathbb{A}^1(\alpha_i)$ and $\bigvee_1(\alpha_i)$, respectively, where i is a unique index for the occurrence concerned in f.

Let $f, g: m \to n$ be networks, and let $1, \ldots, k$ and $1, \ldots, l$ be the indices for the occurrences of \mathbb{A}^1 and \bigvee_1 in f and g, respectively. f and g are *Broy equivalent*, written $f \equiv_{\text{broy}} g$, iff for all oracles $\alpha_1, \ldots, \alpha_k \in \{1, 2\}^{\infty}$, there exists oracles $\beta_1, \ldots, \beta_l \in \{1, 2\}^{\infty}$ such that

$$(*) \quad [f(\alpha_1,\ldots,\alpha_k)] = [g(\beta_1,\ldots,\beta_l)]$$

holds and reverse, for all oracles $\beta_1, \ldots, \beta_l \in \{1, 2\}^{\infty}$, there exists oracles $\alpha_1, \ldots, \alpha_k \in \{1, 2\}^{\infty}$ such that (*) holds.

f and g are Broy-fair equivalent, written $f \equiv_{\text{broy-fair}} g$, iff $f \equiv_{\text{broy}} g$ and all α 's and β 's are fair. An oracle $\alpha \in \{1, 2\}^{\infty}$ is fair iff $|\alpha^{-1}(1)| = |\alpha^{-1}(2)|$.

In Section 6.3, we will write $[f]_{\text{broy}}(\sigma_1, \ldots, \sigma_m)$ for $\bigcup \{ [f(\alpha_1, \ldots, \alpha_k)](\sigma_1, \ldots, \sigma_m) \mid \alpha_1, \ldots, \alpha_k \in \{1, 2\}^{\infty} \}$. \Box

Various interesting models for process algebra are obtained by defining equivalence relations on process graphs. For a systematic treatment of most of these equivalence relations, the reader is referred to [15]. We mention:

 $\equiv_{\rm ct}$ completed trace equivalence,

 ${\rm \dot{t}}_{\rm w}$ weak bisimulation equivalence,

 $\overleftrightarrow_{\rm b}$ branching bisimulation equivalence.

Weak and branching bisimulation were introduced, in the setting of ACP, in [32] and [14], respectively. $P \equiv_{ct} Q$ iff trace(P) = trace(Q). The above-mentioned equivalences on process graphs naturally induce corresponding equivalences on asynchronous dataflow networks. For example, the equivalence induced by \equiv_{ct} corresponds to Jonsson's trace model [5].

Definition 6.7 (\equiv_{trace}) Let f = (m, n, P) and g = (p, q, Q) be two networks. f and g are trace equivalent, written $f \equiv_{\text{trace}} g$, iff m = p, n = q and $P \equiv_{\text{ct}} Q$. \Box

Another interesting equivalence on asynchronous dataflow networks induced by the above-mentioned equivalences on process graphs is the following.

Definition 6.8 (\equiv_{bisim})

Let f = (m, n, P) and g = (p, q, Q) be two networks. f and g are bisimulation equivalent, written $f \equiv_{\text{bisim}} g$, iff m = p, n = q and $P \rightleftharpoons_{\text{b}} Q$. \Box

After the next subsection, which explains the time anomaly in the history model, the models derived in this subsection are related to each other.

6.2 Time Anomaly

In this subsection, the time anomaly is illustrated by means of two examples: the Brock-Ackermann example [9] and an example originating from Russell [10].

Example 6.9 (Brock-Ackermann example)

The Brock-Ackermann example is depicted in Figure 3. Here \bigvee_1 and \aleph^1 are the merge and copy constants for asynchronous dataflow networks defined in Section 5.2. The atomic cells used in this example are:

The following networks are built from these atomic cells:

 $\begin{aligned} f &= (\text{DUP} + \text{SUC} \circ \text{DUP}) \circ \bigvee_1 \circ 2\text{BUF} \circ \aleph^1 \\ f' &= (\text{DUP} + \text{SUC} \circ \text{DUP}) \circ \bigvee_1 \circ \mathsf{I}_1 \circ \aleph^1 \end{aligned}$

It is easy to see that the networks f and f' realize the same relation between their input and output streams, i.e. $f \equiv_{\text{history}} f'$. However, f and f' can not always be substituted for each other in a larger network. Consider, for instance, the networks $f \uparrow^1$ and $f' \uparrow^1$. The stream 1223... is in $[f' \uparrow^1](1)$ but not in $[f \uparrow^1](1)$ because 2BUF must have consumed both 1's yielded by the duplication of the input before the feedback loop can contribute to the output. So $f \uparrow^1 \not\equiv_{\text{history}} f' \uparrow^1$. \Box



Figure 3: Brock-Ackermann example

Example 6.10 (Russell's example)

In the previous example, the atomic cells were all deterministic. The merge constant introduced nondeterminism in that example. Russell's example shows that the time anomaly may also occur by nondeterministic atomic cells. Consider the following atomic cells:

$$g = (1, 1, ((er_1(x); s_1(0) \cdot s_1(1)) + s_1(0) \cdot (er_1(x); s_1(0))) * \delta)$$

$$g' = (1, 1, ((er_1(x); s_1(0) \cdot s_1(1)) + s_1(0) \cdot (er_1(x); s_1(0)) + s_1(0) \cdot (er_1(x); s_1(1))) * \delta)$$

It is easy to see that the atomic cells g and g' realize the same relation between their input and output streams, i.e. $g \equiv_{\text{history}} g'$. Note further that the possible output streams are independent of the input streams. However, the stream 01... is contained in $[(g' \circ \mathbb{A}^1) \uparrow^1]()$ but not in $[(g \circ \mathbb{A}^1) \uparrow^1]()$, because g must already have produced one datum before it can contribute a 0 followed by a 1 to the output. So $(g \circ \mathbb{A}^1) \uparrow^1 \not\equiv_{\text{history}} (g' \circ \mathbb{A}^1) \uparrow^1$. \Box

6.3 Comparison of models

We have defined the following equivalences on networks in the process algebra model for asynchronous dataflow:

$\equiv_{\rm history}$	history equivalence,
$\equiv_{\rm broy}$	Broy equivalence,
$\equiv_{\rm broy-fair}$	Broy-fair equivalence,
\equiv_{trace}	trace equivalence,
$\equiv_{\rm bisim}$	bisimulation equivalence.

The process algebra model modulo the first four equivalences yields Kahn's history model [3], Broy's oracle-based models [4] and Jonsson's trace model [5], respectively. Thus, we know from the relevant literature that the first of these equivalences is not a congruence (in [9] is shown that the history model is not compositional) and that the others are congruences (in [4] and [5] is shown that the Broy models and the trace model are compositional). In [5], it is further shown that the trace model is fully abstract with respect to the history model. The bisimulation equivalence corresponds to the process algebra model itself – where processes are considered to be equal iff they are branching bisimulation equivalent. The following summarizes how the above-mentioned equivalences are related (thus showing the connections between the various models):

$$\equiv_{\text{bisim}} \stackrel{(1)}{\not\subseteq} \equiv_{\text{broy}} \stackrel{(2)}{\not\subseteq} \equiv_{\text{broy-fair}}, \quad \equiv_{\text{bisim}} \stackrel{(3)}{\subset} \equiv_{\text{trace}}, \quad \equiv_{\text{broy}} \stackrel{(4)}{\subset} \equiv_{\text{trace}} \stackrel{(5)}{\subset} \equiv_{\text{history}}$$

The incomparibilities (1) and (2) are shown in Example 6.11 and Example 6.12, respectively. The inclusion (3) follows trivially from the inclusion of the corresponding equivalences for processes (see e.g. [31]). It is obvious from the definition of \equiv_{broy} that $\equiv_{\text{broy}} \subseteq \equiv_{\text{history}}$. Because of the compositionality of the Broy model and the full abstractness of the trace model, the inclusion (4) follows then immediately, except for its strictness. In Example 6.13 is shown that the inclusion (4) is strict. The full abstractness of the trace model with respect to the history model and the non-compositionality of the history model entail directly the strict inclusion (5). The proofs below are quite sketchy. In the case of equivalences like \equiv_{broy} in Example 6.11, such statements require a further formal proof using invariants that we have not included. For use of invariants in the process algebra setting, we refer to [33, 34].

Example 6.11 (\equiv_{broy} and \equiv_{bisim} are incomparable)

First, we give an example of two networks which are bisimulation equivalent, but not Broy equivalent. The following atomic cells occur in the networks concerned:

SOURCE
$$(i) = (0, 1, \tau \cdot (s_1(i)^* \delta))$$

FILTER_0 = $(1, 1, \tau \cdot (er_1(x); (s_1(0) \triangleleft x = 0 \triangleright \tau))^* \delta)$

Let f = SOURCE(0) and $g = (\text{SOURCE}(0) + \text{SOURCE}(1)) \circ \bigvee_1 \circ \text{FILTER_O}$. Then (i) $f \not\equiv_{\text{broy}} g$ and (ii) $f \equiv_{\text{bisim}} g$. For (i), we see that $[f]_{\text{broy}}() = \{0^{\infty}\}$ and $[g]_{\text{broy}}() = \{0^{\infty}, \varepsilon\}$ (g produces the ε output for the completely unfair oracle $\alpha = 2^{\infty}$). For (ii), notice that the corresponding processes are branching bisimilar because: (a) in each state after a number of τ s an output is generated and (b) no other outputs may be generated.

Next, we give an example of networks which are Broy equivalent, but not bisimulation equivalent. Let $f = \mathbb{A}^1 \circ (\mathbb{I}_1 + \mathbb{A}^1)$ and $g = \mathbb{A}^1 \circ (\mathbb{A}^1 + \mathbb{I}_1)$. Then (i) $f \equiv_{\text{broy}} g$ and (ii) $f \not\equiv_{\text{bisim}} g$. For (i), we see that for each pair of oracles for f one may find a pair of oracles for g which produces the same output stream as f and conversely. For (ii), we see that the branching structure of f and g differ. \Box

Example 6.12 (\equiv_{broy} and $\equiv_{\text{broy-fair}}$ are incomparable)

The first example from 6.11 provides two networks which are Broy-fair equivalent, but not Broy equivalent. For the reversed non-inclusion, we use an additional atomic cell $STOP_AT_1 = (1, 1, stop_at_1)$ where

 $stop_at_1 = \tau \cdot (r_1(0) \cdot s_1(0) \cdot stop_at_1 + r_1(1) \cdot (er_1(x); \tau) * \delta)$

Let $f = \text{SOURCE}(0) \circ \mathbb{A}^1 \circ (\mathbf{b}^1 + \mathbf{I}_1)$ and $g = (\text{SOURCE}(0) + \text{SOURCE}(1)) \circ \mathbf{b}_1 \circ \text{STOP}_AT_1$. Then $f \equiv_{\text{broy}} g$, but $f \not\equiv_{\text{broy-fair}} g$. \Box

The following example conforms the remark in [35] that \equiv_{broy} is not fully abstract, in view of the fact that \equiv_{trace} is fully abstract, cf. e.g. [5].

Example 6.13 (\equiv_{broy} is strictly included in \equiv_{trace})

Let $f = \mathbb{A}^1 \circ \bigvee_1$ and $f' = \mathbb{A}^1 \circ (2BUF + f) \circ \bigvee_1$ (2BUF is the component that appears in Example 6.9). Then, for $D = \{0, 1\}$, (i) $f \not\equiv_{\text{broy}} f'$ and (ii) $f \equiv_{\text{trace}} f'$. For f we need two oracles $\alpha_s, \alpha_m \in \{1, 2\}^{\infty}$ for the split and the merge components, respectively; and for f' we need two more oracles $\alpha'_s, \alpha'_m \in \{1, 2\}^{\infty}$ for the additional components. For (i), we see that the function computed by f' for oracles α'_s and α'_m with the prefix 11 can not be computed by f for any two oracles α_s and α_m . Suppose the contrary. Then there are two oracles $\alpha_s = s_0 s_1 s_2 \dots$ and $\alpha_m = m_0 m_1 m_2 \dots$ such that $[f(\alpha_s, \alpha_m)](0) = \{\varepsilon\}$ and $[f(\alpha_s, \alpha_m)](01) = \{01\}$. Since $[f(\alpha_s, \alpha_m)](0) = \{\varepsilon\}$, $s_0 \neq m_0$. So for the input 01 two cases are left, $s_1 \neq m_0$ and $s_1 = m_0$, which both lead to contradiction: $[f(\alpha_s, \alpha_m)](01) = \{\varepsilon\}$ if $s_1 \neq m_0$, and $[f(\alpha_s, \alpha_m)](01) = \{1\}$ or $[f(\alpha_s, \alpha_m)](01) = \{10\}$ (depending on m_1) if $s_1 = m_0$. For (ii), we see immediately that each trace of f is a trace of f' as well. And conversely, we see that every trace w of f' has the property that, for all $d \in D$ and $n \in \mathbb{N}$, $card\{i \mid i \leq n, w_i = s_1(d)\} \leq i \leq n$ $card\{i \mid i \leq n, w_i = r_1(d)\}$. Now, every trace with this property is a trace of f because: (a) the split component may deliver all 0s to the "left" and all 1s to the "right" and (b) the merge component may always consume from the left if a 0 is necessary to produce a trace with the property above and from the right if a 1 is necessary. \Box

Note the following. Let $\alpha_s = (12)^{\infty}$ and $\alpha_m = (21)^{\infty}$ be oracles for the split connection and the merge connection of the network f in Example 6.13 above. Then $[f(\alpha_s, \alpha_m)](d_2d_1d_4d_3...) = [2BUF](d_1d_2d_3d_4...)$. Hence, if D has only one element, e.g. $D = \{0\}$, then f and f' are Broy (and Broy-fair) equivalent.

6.4 Guess-and-borrow queues

Different from a synchrononous dataflow network, an asynchronous one may delay the use of its resources. In case asynchronism is exploited fully, it should also be possible to use the resources in advance.

In the Brock-Ackermann example (Example 6.9) $[f' \uparrow^1](1)$ contains 1223..., which is not in $[f \uparrow^1](1)$ under the interpretation of dataflow networks where wires are treated as unbounded queues. With a more powerful kind of identity connections, viz. guessand-borrow queues, this anomaly disappears.

A guess-and-borrow queue may deliver any number of arbitrary data to a cell while it is empty. However, if this turns out not to be in agreement with the actual data subsequently received, the cell has to drop the computation based on the wrong data.

In the Brock-Ackermann example, 1223... is a common output stream of $f \uparrow^1$ and $f' \uparrow^1$ if the identity connections are interpreted as (unbounded) guess-and-borrow queues. One only needs such a queue before the 2BUF cell; which now may borrow the necessary data in order to annihilate the differences between the 2BUF cell and an identity connection. More generally, one may see that $f \uparrow^1$ and $f' \uparrow^1$ compute the same input-output relation on streams with this more powerful operational interpretation for the identity connections. A similar simple argument works in the case of Russell's example as well.

We now briefly outline the construction of a process algebra model which can be regarded as the operational model of asynchronous dataflow networks with guess-andborrow queues. In the style of Parrow [36], we use as processes pairs (p, U) with p a process modulo $\begin{subarray}{l}{\hookrightarrow}_{b\Delta}$ (divergence sensitive branching bisimulation)² and U a collection of admissible complete traces for p. The process algebra operators are to be extended to the trace set component. This is straightforward, except for the point that in $X \parallel Y$ complete traces must be formed by merging complete traces for both X and Y in such a way that all actions are "used". We also define $\operatorname{trace}(p, U) = \operatorname{trace}(p) \cap U$. With these ingredients we may give a process algebra definition of the identity constant as a guess-and-borrow queue as follows. The identity constant, called *stream retimer*, is the wire $I_1 = (1, 1, \operatorname{sr}_1^1)$, where $\operatorname{sr}_1^1 = (p, U)$ and

$$p = \left(\sum_{d \in D} r_1(d) + \sum_{d \in D} s_1(d)\right)^* \delta$$

 $U = \{ \alpha \mid \alpha \text{ is complete trace over } \{r_1(d), s_1(d) \mid d \in D\}, \mathsf{stream}_1^{in}(\alpha) = \mathsf{stream}_1^{out}(\alpha) \}$

It can be shown that $I_1 \circ I_1 = I_1$, so I_1 is an identity flow. However, this identity flow allows to shift a stream forward and backward in time.

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 $^{^{2}}$ In case of divergence sensitive bisimulation, if two nodes are related by a bisimulation and one node permits an infinite path of silent steps, the other node must also permit an infinite path of silent steps. Divergence sensitive bisimulation was introduced in [30].

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Appendix

We write [n], where $n \in \mathbb{N}$, for the set $\{1, \ldots, n\}$.

We write $x \uparrow y$, where $x = (x_1, \ldots, x_m)$ and $y = (y_1, \ldots, y_n)$ are tuples, for the tuple $(x_1, \ldots, x_m, y_1, \ldots, y_n)$.

We usually write $\langle x, y \rangle$ instead of (x, y).

We use the following notation for sequences:

ε	the empty sequence;
x	the sequence having x as sole element;
$\sigma_1 \sigma_2$	the concatenation of the sequences σ_1 and σ_2 ;
$ \sigma $	the length of the sequence σ ;
$hd(\sigma)$	the head of the sequence σ ;
$tl(\sigma)$	the tail of the sequence σ ;
$\sigma(n)$	the element of the sequence σ with index n .